

## ABSTRACT OF THE DISCLOSURE

A method for testing an output circuit of a semiconductor device including a plurality of output circuits includes the step of  
5 turning ON p-ch and n-ch MIS transistors of a subject output circuit, turning ON and OFF one and the other, respectively, of p-ch and n-ch MIS transistors of another output circuit used as a reference output circuit, measuring the potential difference  
10 between the output terminal of the subject output circuit and the output terminal of the reference output circuit and the penetrating current of the subject output circuit, and calculating the ON-resistance of the p-ch or n-ch transistor of the subject output circuit.

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